

WHAT IS CLAIMED IS:

1. A class D amplifier comprising:

a modulating circuit which modulates a input signal into
a pulse signal;

5 a first driving circuit which drives a first output

transistor which outputs a signal level corresponding to a

high power supply to an output terminal;

a second driving circuit which drives a second output

transistor which outputs a signal level corresponding to a

10 lower power supply to the output terminal,

wherein the pulse signal is ^{supplied} ~~supplied~~ to the first and

second driving circuits to complementarily drive the first and

second output transistors;

a first detecting circuit disposed in the first driving

15 circuit, which detects an overcurrent flowing through the

first output transistor to output a first signal;

a second detecting circuit disposed in the second driving

circuit, which detects an overcurrent flowing through the

second output transistor to output a second signal; and

20 a gate circuit which, in response to one of the first and

second signals, blocks the pulse signal to be input to the

first and second driving circuits.